



GR 97 P 2681

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By: JPZ

Date: 8/4/03

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TECHNOLOGY CENTER 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Albrecht Mayer
Applic. No. : 09/164,123
Filed : September 30, 1998
Title : Method for Assembling Integrated Circuits
With Protection Of The Circuits Against
Electrostatic Discharge, And Arrangement Of
Integrated Circuits With Protection Against
Electrostatic Discharge

Examiner : Julio J. Maldonado

Group Art Unit : 2823

A M E N D M E N T

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Responsive to the Office action dated May 20, 2003 kindly
amend the above-identified application as follows: